

#9/10

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
(Case No. RA001C10)

In the Application of: )  
FARMWALD ET AL. )  
Serial No: 09/514,872 )  
Filed: FEBRUARY 28, 2000 )  
Title: METHOD AND APPARATUS FOR )  
CONTROLLING A SYNCHRONOUS )  
MEMORY DEVICE )

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Group  
Art Unit: 2781

Before  
Examiner: G. Auve'

Assistant Commissioner for Patents  
Washington, DC 20231

FAX RECEIVED

AMENDMENT

DEC 01 2000

PETITIONS OFFICE

Dear Sir:

Kindly amend the application as follows:

IN THE CLAIMS:

Kindly Amend the following claims:

2  
152. (Amended) The method of claim 151 wherein the write request  
includes an operation code[, the first portion of data and the second  
portion of data are included in a packet].

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3  
153. (Amended) The method of claim 152 wherein the memory device  
receives the operation code synchronously with respect to an edge  
transition of the external clock signal [write request, the first